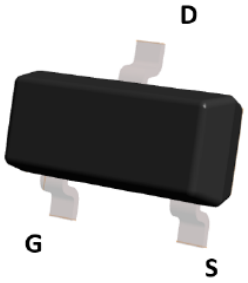
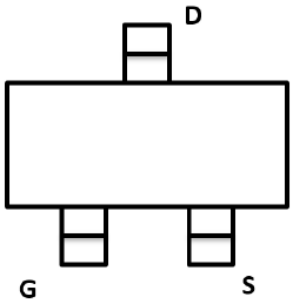
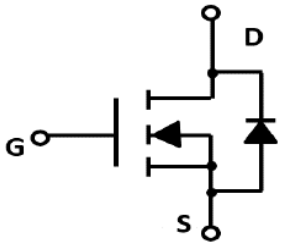
N-Channel Enhancement Mode Field Effect Transistor



Top View



SOT-23



Product Summary

- V_{DS} 40V
- I_D 5.0A
- $R_{DS(ON)}$(at V_{GS}=10V) ＜45mohm
- $R_{DS(ON)}$(at V_{GS}=4.5V) ＜60mohm
- 100% ∇V_{DS} Tested

General Description

- Trench Power LV MOSFET technology
- High Power and current handing capability
- Part no. with suffix “Q” means AEC-Q101 qualified

Applications

- PWM application
- Load switch

■ Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-source Voltage		V_{DS}	40	V
Gate-source Voltage		V_{GS}	±20	V
Drain Current	T_A=25℃	I_D	5	A
	T_A=70℃		4	
Pulsed Drain Current [A]		I_{DM}	20	A
Total Power Dissipation	T_A=25℃	P_D	1.2	W
	T_A=70℃		0.8	W
Thermal Resistance Junction-to-Ambient [B]		$R_{\theta JA}$	104	℃/ W
Junction and Storage Temperature Range		T_J ,T_{STG}	-55～+150	℃

■ Ordering Information (Example)

PREFERED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJL05N04AQ	F2	4005.	3000	30000	120000	7“ reel

■ Electrical Characteristics (T_J=25℃ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=250μA	40			V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=40V,V_{GS}=0V			1	μA
Gate-Body Leakage Current	I_{GSS1}	V_{GS}=±20V, V_{DS}=0V			±100	nA
	I_{GSS2}	V_{GS}=±10V, V_{DS}=0V			±50	
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}= V_{GS}, I_D=250μA	1.0	1.5	2.5	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D=5A		30	45	mΩ
		V_{GS}=4.5V, I_D=3A		40	60	
Diode Forward Voltage	V_{SD}	I_S=5A,V_{GS}=0V			1.2	V
Dynamic Parameters						
Input Capacitance	C_{iss}	V_{DS}=10V,V_{GS}=0V,f=1MHZ		417		pF
Output Capacitance	C_{oss}			73		
Reverse Transfer Capacitance	C_{rss}			59		
Switching Parameters						
Total Gate Charge	Q_g	V_{GS}=10V,V_{DS}=20V,I_D=3.5A		9.76		nC
Gate-Source Charge	Q_{gs}			1.85		
Gate-Drain Charge	Q_{gd}			2.14		
Reverse Recovery Chrage	Q_{rr}	I_F=3.5A, di/dt=100A/us		2.6		
Reverse Recovery Time	t_{rr}			19.1		ns
Turn-on Delay Time	$t_{D(on)}$	V_{GS}=10V, V_{DS}=20V, I_D=3.5A R_{GEN}=3Ω		4.4		
Turn-on Rise Time	t_r			21		
Turn-off Delay Time	$t_{D(off)}$			12		
Turn-off fall Time	t_f			19.6		

A. Pulse Test: Pulse Width≤300us,Duty cycle ≤2%.

B. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance, where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design, while $R_{\theta JA}$ is determined by the board design. The maximum rating presented here is based on mounting on a 1 in 2 pad of 2oz copper.

■ Typical Performance Characteristics

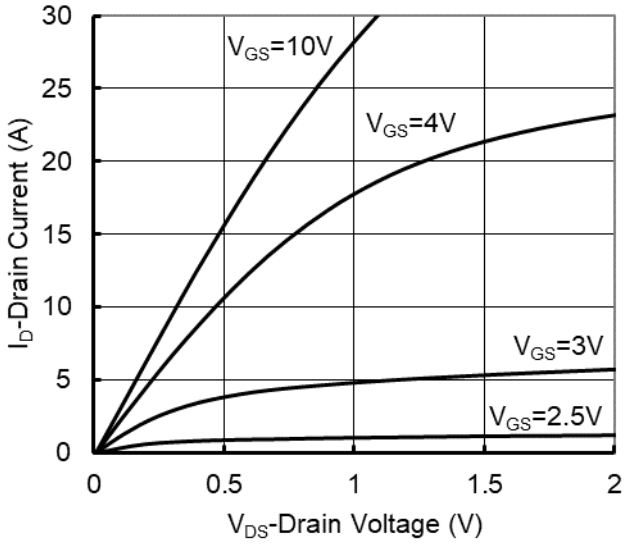


Figure1. Output Characteristics

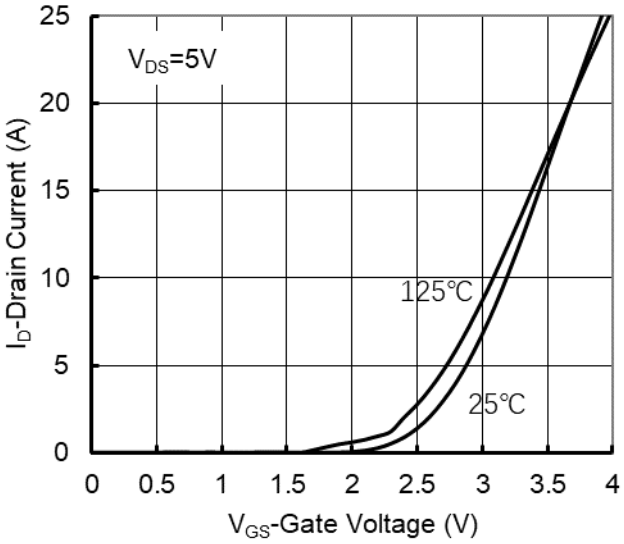


Figure2. Transfer Characteristics

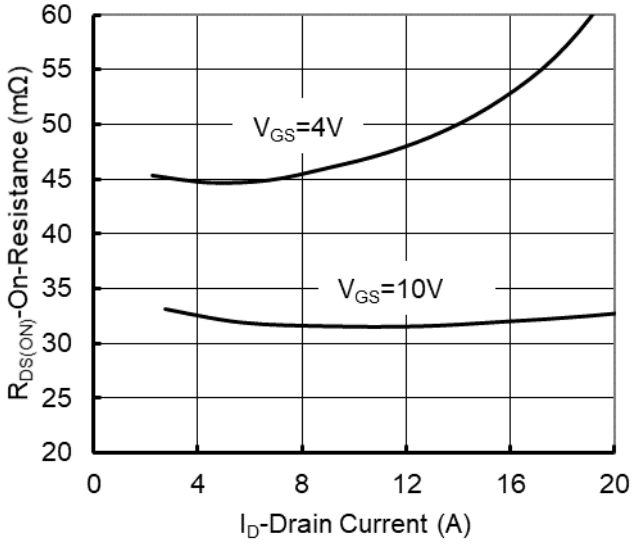


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

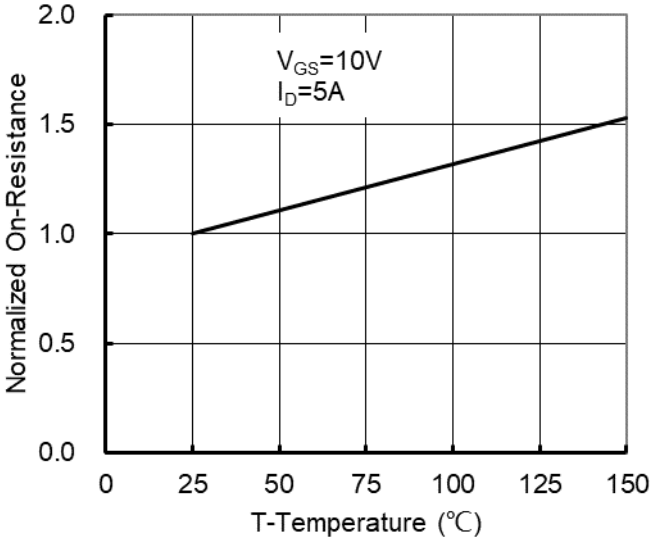


Figure 4: On-Resistance vs. Junction Temperature

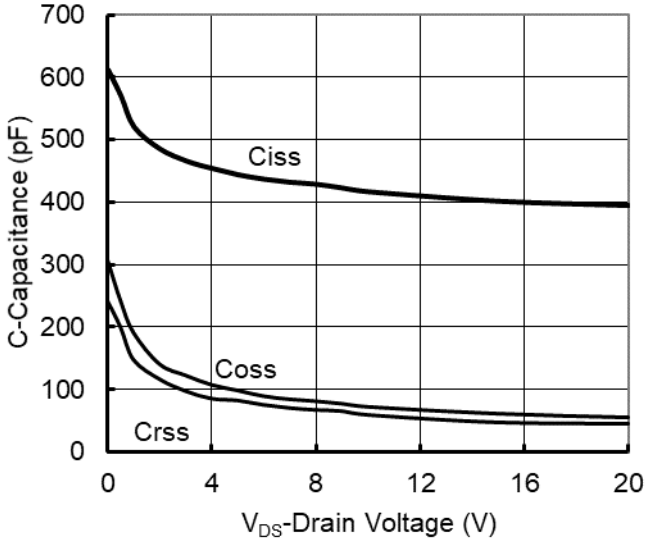


Figure5. Capacitance Characteristics

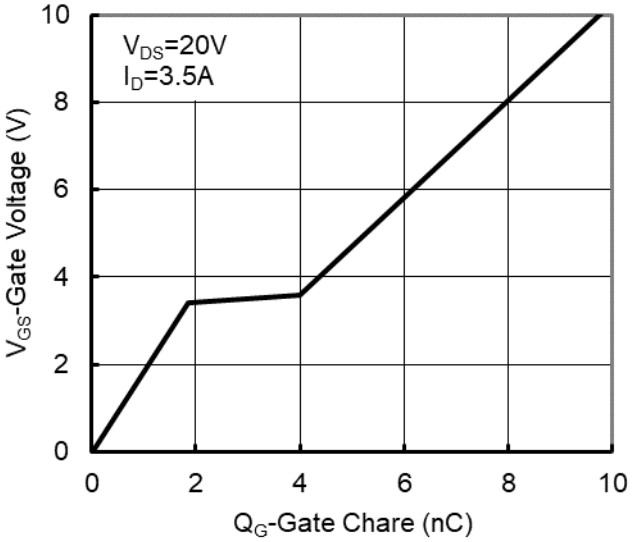


Figure6. Gate Charge

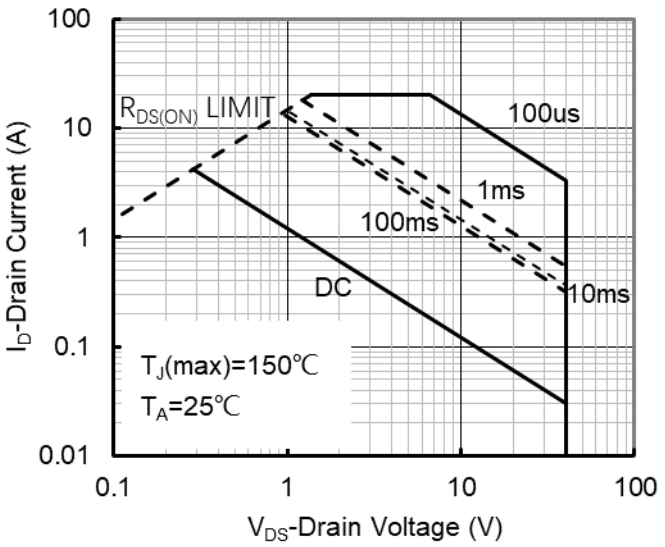


Figure7. Safe Operation Area

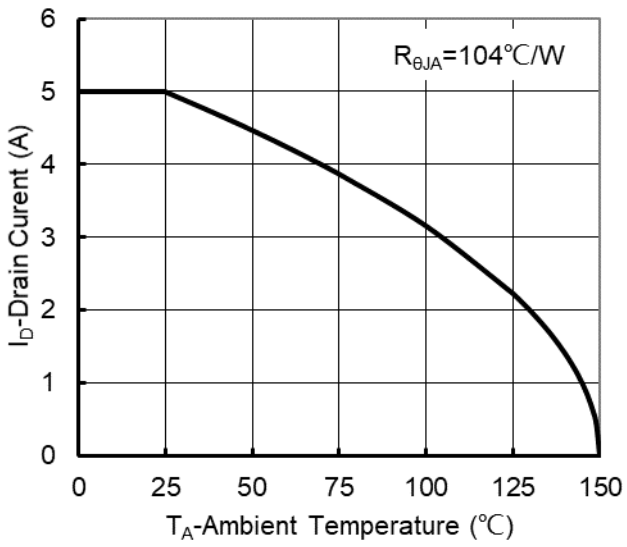


Figure8. Maximum Continuous Drain Current vs Ambient Temperature

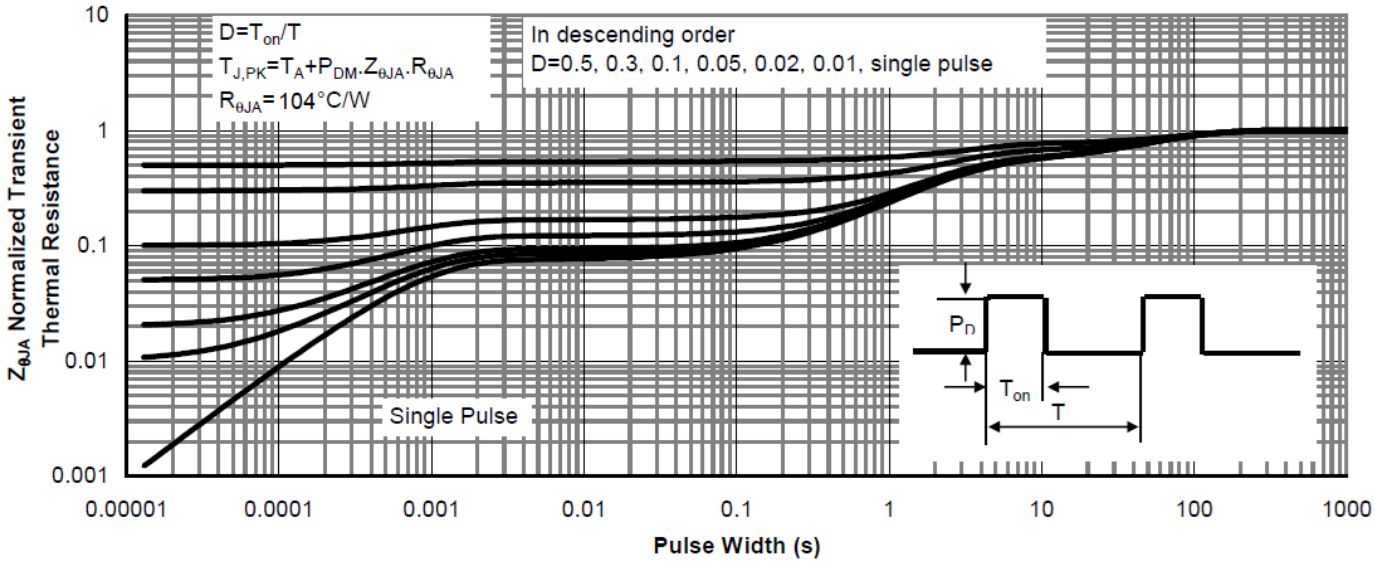
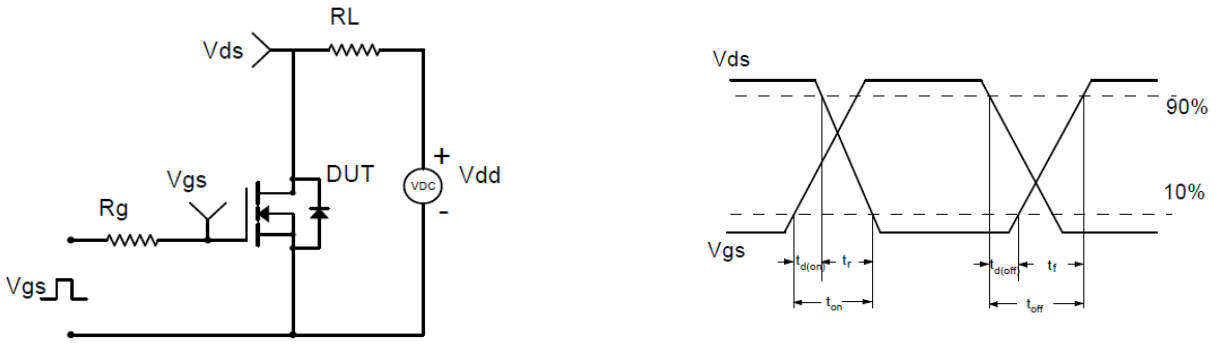
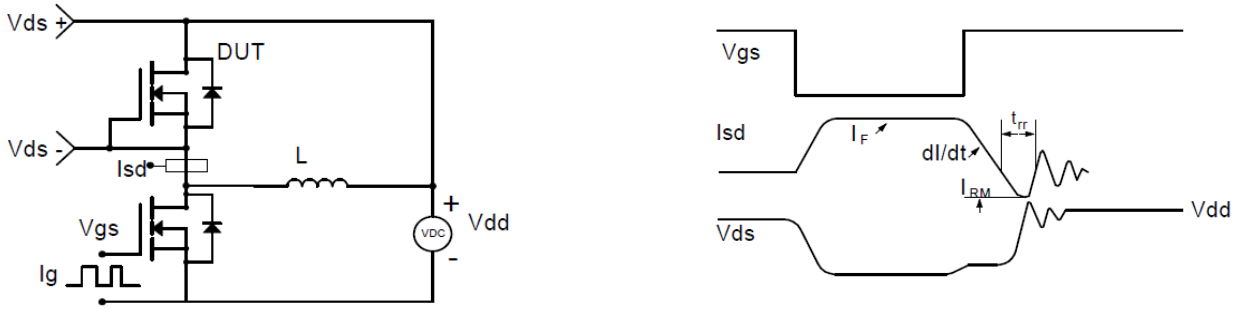


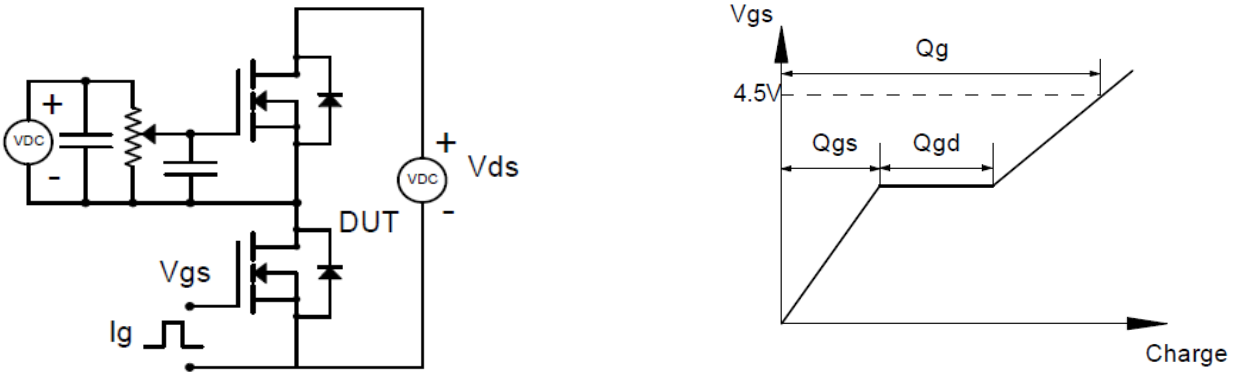
Figure9. Normalized Maximum Transient Thermal Impedance



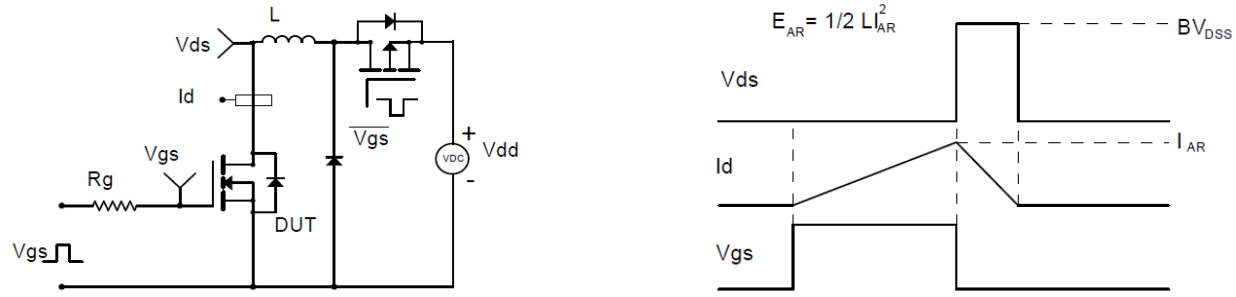
Resistive Switching Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms

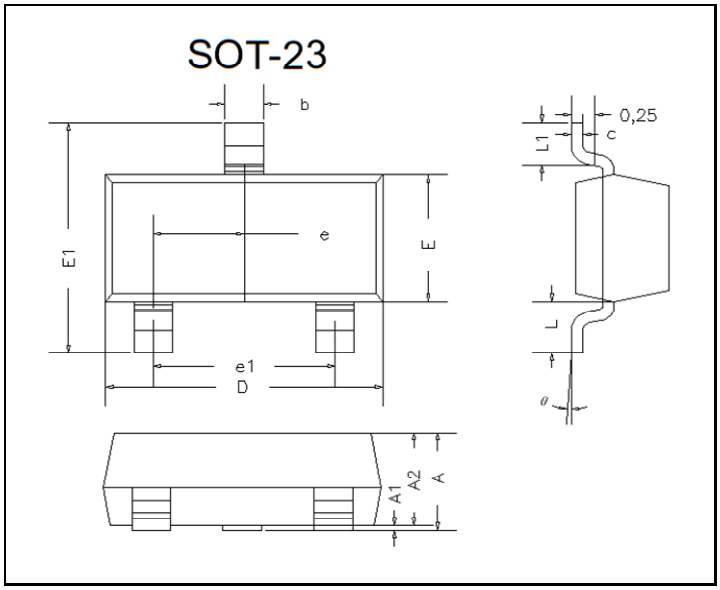


Gate Charge Test Circuit & Waveform



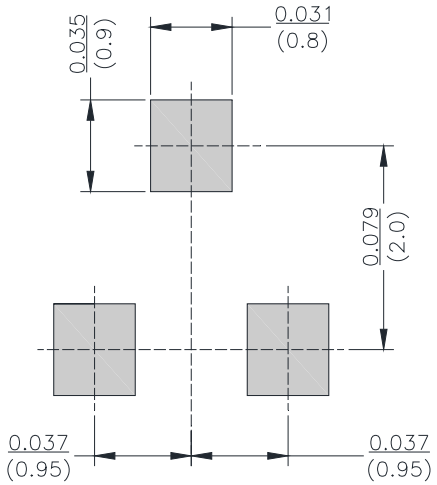
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

■ SOT-23 Package information



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.035	0.045	0.90	1.15	
A1	0.000	0.004	0.00	0.10	
A2	0.035	0.041	0.90	1.05	
b	0.012	0.020	0.30	0.50	
c	0.004	0.008	0.10	0.20	
D	0.110	0.118	2.80	3.00	
E	0.047	0.055	1.20	1.40	
E1	0.089	0.100	2.25	2.55	
e	0.370TYP		0.95TYP		
e1	0.071	0.079	1.80	2.00	
L	0.220REF		0.55REF		
L1	0.012	0.020	0.30	0.50	
θ	0°	8°	0°	8°	

■ SOT-23 Suggested Pad Layout



Disclaimer

The information presented in this document is for reference only. Yangzhou Yangjie Electronic Technology Co., Ltd. reserves the right to make changes without notice for the specification of the products displayed herein to improve reliability, function or design or otherwise.

The product listed herein is designed to be used with automotive electronics， are not designed for use in medical, life-saving, lifesustaining,or military,Yangjie or anyone on its behalf, assumes no responsibility or liability for any damages resu lting from such improper use of sale.

This publication supersedes & replaces all information previously supplied. For additional information, please visit our website http:// www.21yangjie.com , or consult your nearest Yangjie's sales office for further assistance.